

Capacitors in Series – Balancing Resistors

When aluminium electrolytic capacitors are connected as a bank of capacitors where there are multiple parallel branches of series capacitors, the need for balancing resistors is diminished due to the averaging effect of the number of capacitors where individual leakage current values become less critical. However, in the case of just two capacitors connected in series it is advisable to use balancing resistors in order to control the voltage sharing across each device due to imbalances in individual capacitor leakage currents. Figure 13 shows two capacitors in series along with balancing resistors.

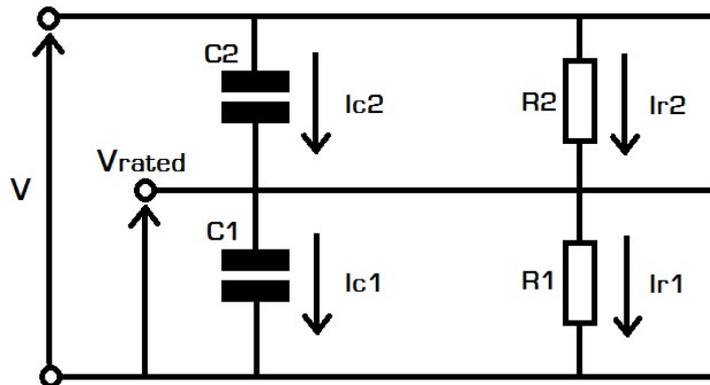


Figure 13

Trying to calculate individual leakage current values is impractical in a production environment and so an estimation based on experience can be made in order to calculate the differences in leakage currents between two capacitors in series. In figure 12 the individual leakage currents are I_{c1} and I_{c2}.

The estimation for the leakage current difference between I_{c1} and I_{c2} is:

$$\text{Difference, mA} = (0.0015 \cdot C \cdot V) / 2000$$

where C is the value of the individual capacitors in μF and V is the voltage across both capacitors in series.

Next, to calculate the value for balancing resistors, the equation is:

$$R \Omega = ((2 \cdot V_{\text{rated}}) - V) / I_{\text{difference}}$$

where V_{rated} is the individual capacitor rated voltage and V is the voltage across both capacitors in series.

Next to calculate the power rating of the resistors, we firstly require the current through the balancing resistor and then the power. The current through the balancing resistor is:

$$I, \text{ mA} = (V/2) / R$$

where V is the voltage across both capacitors in series and R is the value of a balancing resistor.

The power, P, W = I²R.

Example: V = 940v, V_{rated} = 500v, C = 1800 μF

$$I_{\text{difference}} = 2.538 \text{ mA}$$

$$R = 23.64 \text{ k} \Omega$$

$$I = 19.881 \text{ mA} = 0.019881 \text{ A}$$

$$P = 9.34 \text{ W}$$

